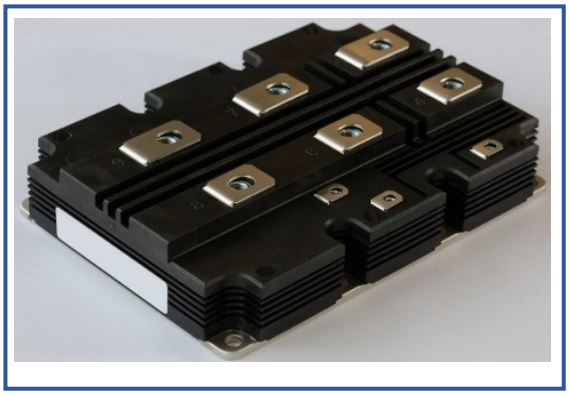
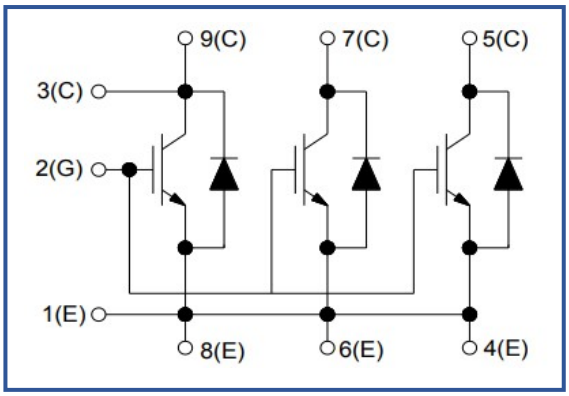
High Voltage IGBT Module

6500 V 750 A





Chip features

- IGBT chip
  - Low $V_{CE(sat)}$ value
  - 10 µs short circuit of 125°C
  - Square RBSOA of 2xI_C
- FRD chip
  - Fast and soft reverse recovery
  - Low voltage drop

Design features

- AlSiC baseplate
- AlN DBC substrate
- Improved termal cycling
- RoHS compliant

Typical application

- Traction motor drives
- High power converters
- Multilevel inverters

Maximum rated values

Definition	Symbol	Conditions	Value	Unit
IGBT				
Collector-Emitter voltage	V_{CES}	V_{GE} = 0.	6500	V
Maximum allowable collector current (continuous)*2	$I_{C\ 25}$	$T_{vj\ (max)}$ = 150°C; T_c = 25°C.	-	A
	$I_{C\ 80}$	$T_{vj\ (max)}$ = 150°C; T_c = 80°C.	750	A
Repetitive peak collector current*1	I_{CRM}	I_{CRM} = 2 x $I_{C\ nom}$; t_p = 1 ms.	1500	A
Short-circuit duration	t_{psc}	T_{vj} = 25°C; V_{GE} = ±15 V; V_{CE} = 4500 V; $I_{C\ max}$ < 2800 A.	10	µs
		T_{vj} = 125°C; V_{GE} =±15 V; V_{CE} = 4500 V; $I_{C\ max}$ < 2800 A.	10	
Gate-Emitter voltage	V_{GES}		±20	V
Junction operating temperature	$T_{vj\ (op)}$		-40…+125	°C
Inverse diode \ Freewheeling diode				
Repetitive peak reverse voltage	V_{RRM}	V_{GE} = 0 V.	6500	V
Maximum allowable forward current (continuous)*2	$I_{F\ 25}$	$T_{vj\ (max)}$ = 150°C; T_c = 25°C.	-	A
	$I_{F\ 80}$	$T_{vj\ (max)}$ = 150°C; T_c = 80°C.	750	A
Repetitive peak forward current*1	I_{FRM}	I_{FRM} = 2 x $I_{F\ nom}$; t_p = 1 ms.	1500	A
Junction operating temperature	$T_{vj\ (op)}$		-40…+125	°C
Module				
Storage temperature	T_{stg}		-40…+60	°C
Isolation voltage	V_{isol}	AC sin 50 Hz; t = 1 min.	10200	V

*1 Pulse width and repetition rate should be such that device junction temperature does not exceed maximum T_{vj} rating

Characteristics

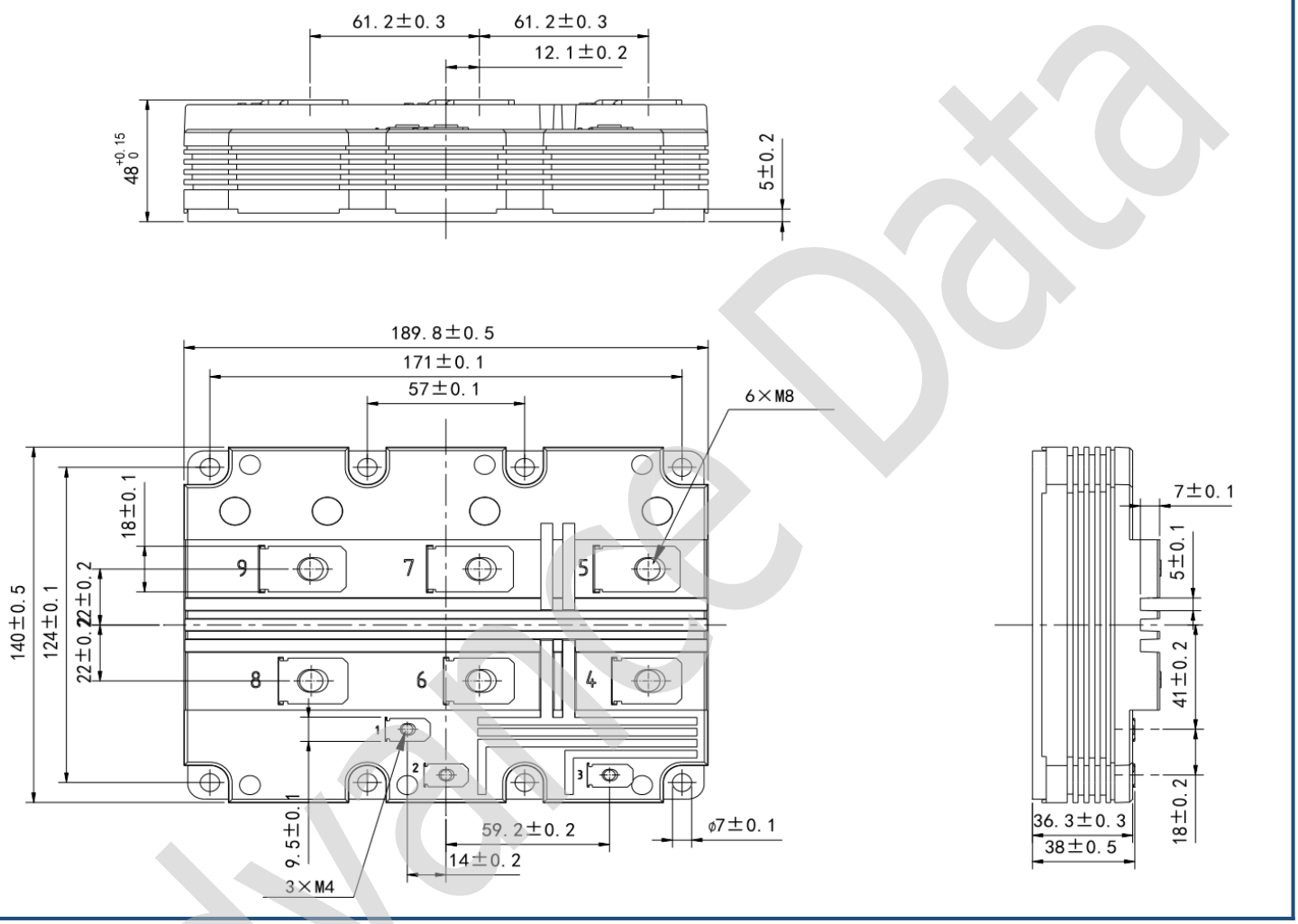
Definition	Symbol	Conditions		Value min.	Value typ.	Value max.	Unit
IGBT							
Collector-Emitter saturation voltage	V_{CEsat}	V_{GE} = +15 V; I_C =750 A;	T_{vj} = 25°C	-	3.00	3.40	V
			T_{vj} = 125°C	-	3.90	4.30	V
Gate-Emitter threshold voltage	$V_{GE(th)}$	I_C = 120 mA; V_{CE} = V_{GE}; T_{vj} = 25°C; t_u = 2 ms.		5.00	6.00	7.00	V
Collector-Emitter cut-off current	I_{CES}	V_{CE} = 6500 V; t_u = 20 ms; V_{GE} = 0.	T_{vj} = 25°C	-	-	1.00	mA
			T_{vj} = 125°C	-	-	90.00	mA
Gate-Emitter leakage current	I_{GES}	V_{CE} = 0; V_{GE} = ±20 V; T_{vj} = 25°C;				1.00	µA
Input capacitance	C_{ies}	V_{CE} = 25 V; V_{GE} = 0 V; f = 100 kHz; T_{vj} = 25°C.		-	123	-	nF
Reverse transfer capacitance	C_{res}			-	2.6	-	nF
Total gate charge	Q_G	V_{GE} = - 15÷15 V.		-	9.4	-	µC
Internal gate resistance	R_{Gint}	T_{vj} = 25°C.		-	90.0	-	mΩ
Turn-on delay time	$t_{d(on)}$	V_{CE} = 3900 V; V_{GE} = ±15 V; $I_{C\,max}$ = 750 A; R_G = 1.0 Ω; L = 280 µH.	T_{vj} = 25°C	-	0.67	-	µs
			T_{vj} = 125°C	-	0.66	-	
Rise time	t_{ri}		T_{vj} = 25°C	-	0.33	-	µs
			T_{vj} = 125°C	-	0.34	-	
Turn-on energy	E_{on}		T_{vj} = 25°C	-	4.40	-	J
			T_{vj} = 125°C	-	6.10	-	
Turn-off delay time	$t_{d(off)}$		T_{vj} = 25°C	-	3.06	-	µs
			T_{vj} = 125°C	-	3.09	-	
Fall time	t_{fi}		T_{vj} = 25°C	-	2.39	-	µs
			T_{vj} = 125°C	-	2.98	-	
Turn-off energy	E_{off}		T_{vj} = 25°C	-	3.70	-	J
			T_{vj} = 125°C	-	4.10	-	
Collector-emitter threshold voltage	V_{CE0}	V_{GE} = +15 V; T_{vj} = 125°C; I_{CE1} = 188 A; I_{CE2} = 750 A; t_u = 10 µs.		-	-	-	V
On-State slope resistance (IGBT)	r_{CE0}			-	-	-	mΩ
Thermal resistance junction to case	$R_{th(j-c)}$	DC; V_{GE} = +15 V.		-	-	8.50	K/kW
Inverse diode \ Freewheeling diode							
Forward voltage drop	V_F	I_F = 750 A; V_{GE} = 0;	T_{vj} = 25°C	-	2.55	2.90	V
			T_{vj} = 125°C	-	2.90	3.30	V
Reverse recovery time	t_{rr}	V_{GE} = ±15 V; V_{CE} = 3900 V; $I_{C\,max}$ = 750 A; $R_{G\,on}$ = 1.0 Ω; L = 280 µH.	T_{vj} = 25°C	-	-	-	µs
			T_{vj} = 125°C	-	-	-	µs
Peak reverse current	I_{RM}		T_{vj} = 25°C	-	1310	-	A
			T_{vj} = 125°C	-	1460	-	A
Recovered charge	Q_r		T_{vj} = 25°C	-	1300	-	µC
			T_{vj} = 125°C	-	1680	-	µC
Reverse recovery energy	E_{rec}		T_{vj} = 25°C	-	2.90	-	J
			T_{vj} = 125°C	-	4.08	-	J
Threshold voltage	$V_{(T0)}$	T_{vj} = 125°C; V_{GE} = 0; I_{CE1} = 188 A; I_{CE2} = 750 A; t_u = 10 µs		-	-	-	V
Forward slope resistance	r_T			-	-	-	mΩ
Thermal resistance junction to case	$R_{th(JC-D)}$	DC; V_{GE} = +15 V.		-	-	19.0	K/kW

Module							
Pin resistance	R_{Pxy}	T_{vj} = 25°C.	R_P	-	-	-	mΩ
Parasitic inductance between terminals	L_{Pxy}	T_{vj} = 25°C; f = 1 MHz	L_P	-	10.0	-	nH
Thermal resistance case to heatsink	R_{thCH}	per module		-	-	6	K/kW
Mounting torque for screws to heatsink	M_s	to heatsink M6		-	-	5.00	N*m
Mounting torque for terminal screws	M_t	to terminals M8		-	-	10.0	N*m
Mounting torque for gate terminal	M_t	to terminals M4		-	-	2.00	N*m
Creepage distance	d_s			-	-	56.00	mm
Clearance	d_a			-	-	26.00	mm
Comparative Tracking Index	CTI			-	-	600	
Weight	W			-	-	1.70	kg

Notes:

- Insulating material operating temperature 125°C max;
- The recommended operating junction temperature $T_{vj\ op}$ = - 40 ÷ +125°C.

Overall dimensions: Package type – HV



Part numbering guide

MIHV	-	SS	65	CA	-	750	N	
MIHV								IGBT module package type: HV
		SS						Single switch
			65					Voltage rating (V_{CES}/100)
				CA				IGBT+FRD chipset modification
						750		Current Rating
							N	Climatic version: normal climate